

S/N 09/628,705



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kishore K. Chakravorty
Serial No.: 09/628,705
Filed: July 31, 2000
Title: ELECTRONIC ASSEMBLY COMPRISING INTERPOSER WITH EMBEDDED CAPACITORS AND METHODS OF MANUFACTURE

Examiner: Unknown
Group Art Unit: Unknown
Docket: 884.267US1

COMMUNICATION CONCERNING CO-PENDING APPLICATION(S)

Commissioner for Patents
Washington, D.C. 20231

Applicant would like to bring to the Examiner's attention the following related co-pending application(s) in the above-identified patent application:

<u>Serial No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/631,037	07/31/2000	00884.300US1	ELECTRONIC ASSEMBLY COMPRISING SUBSTRATE WITH EMBEDDED CAPACITORS AND METHODS OF MANUFACTURE
N/A	08/30/2000	00884.315US1	ELECTRONIC ASSEMBLY COMPRISING CERAMIC/ORGANIC HYBRID SUBSTRATE WITH EMBEDDED CAPACITORS AND METHODS OF MANUFACTURE

Respectfully submitted,

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By Applicant's Representatives,

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Date September 14, 2000 By Ann M. McCrackin
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 14 day of September, 2000.

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